

### **Career Objective**

Seeking to work in a competitive work environment (organization), which provides me with opportunities to exercise my knowledge towards its progress and learn further.

### **Experience:**

- Presently working in Sri Datta Electronic Pvt. Ltd Company as Technical Engineer.
  - i. Portable Ground Telemetry system hardware testing and development of rugged and miniaturizing systems, Telemetry software application development and debug process.
  - ii. LNA (Low Noise Amplifier) device Testing .
  - iii. Data Recording and processing in Telemetry Ground checkout system (Telestar which is developed Sri Datta Electronic Pvt. Ltd.,).
  - iv. For NASM-SR from initial zero level, phase-3, Phase-4, Vibration test check level testing has carried out and Integration of different Missiles at DRDO Research Center of Imarat other projects such as VSHROD, PRALAY and Rudram-III.
  - v. Attended Flight trial activity which is held at Balasore ITR, Chandipur which Successfully Tested from it.
  - vi. ATP (Acceptance Test Plan) for Ground Checkout Systems.
  - vii. Training to end user to Operate Ground Telemetry device and Software GUI.
- Worked in Telemetry Division of DRDO Armament Research and Development Establishment Laboratory at Pune, Pashan.
  - i. Programming and Testing of Integrated telemetry module for On Board Telemetry unit and various Telemetry Receiving System from different make such as (L3, GSS and Ulyssix).
  - ii. Preparedness, lab and integrated testing of onboard telemetry unit (OBTU).
  - iii. Testing of Pressure, Acceleration and Gyro sensors, etc. for Flight Telemetry.
  - iv. ATP (Acceptance test Plan) of Integrated Telemetry Modules and Receiver system.
  - v. In hand experience of Sophisticated test & measuring equipment such as Digital Storage Oscilloscope (DSO), Programmable Power supply, Vector Network Analyzer (VNA), Vector signal generator, Spectrum Analyzer and power meter.
  - vi. Calibration of Vector Network Analyzer. Testing of scattering parameters on Vector Network Analyzer.
- Worked at (BARC) Baba Atomic Research center in the Project of Security Surveillance Systems for the Maintained, Installation and Development through ECIL (DAE) Department of Atomic Energy and also Network Support Engineer at (HBNI) Homi Baba National Institute and also worked in EMSD-SEC Troubleshooting and Testing of EVM Machine as a technical support Eng. as JR. Tech. Officer.

- 6 Months experience as VLSI engineer and acquired knowledge in Verilog, VHDL and bus protocols like UART at Scope Integrated Systems.

## **Educational**

<i>Qualification</i>	<i>Branch/ Stream</i>	<i>Year of Passing</i>	<i>College / School</i>	<i>University / Board</i>	<i>% / CGPA</i>
<b>M. Tech</b>	<b>VLSI SD</b>	<b>2018</b>	<b>Barat Institute of Engineering and Technology</b>	<b>JNTUH</b>	<b>8.32</b>
<b>B. Tech</b>	<b>ECE</b>	<b>2015</b>	<b>M.G.I.T, Gandipet</b>	<b>JNTUH</b>	<b>62.17</b>
<b>DIPLOMA</b>	<b>ECE</b>	<b>2012</b>	<b>Govt. Polytechnic College, NZB</b>	<b>SBTET, A. P</b>	<b>76.18</b>
<b>10<sup>th</sup></b>	<b>ALL</b>	<b>2009</b>	<b>G.V.V.H.S, Indalwai</b>	<b>SSC, A. P</b>	<b>81.00</b>

## **Academic Projects:**

### **1) M. Tech Project:**

**Title:** LOW POWER CARRY SAVE ADDER MULTIPLIER USING DOMINO LOGIC

**Team size:** One member

**Platform:** Cadence Virtuoso 6.1.1 V with 180 nm Technology.

#### **Description:**

Multiplier plays a significant role in high processing speed digital signal processing. It's the most important part of the Arithmetic Logic Unit (ALU), FPU and ASIC's where high processing speed is required. Wallace and Vedic multiplier are compared with themselves based on different logic full adders by considering parameters like transistors count and power consumption.

### **2) B. Tech Project:**

**Title:** AN EFFICIENT LOW POWER HIGH SPEED MULTIPLEXER

**Team size:** Three members

**Platform:** MENTOR GRAPHICS EDA TOOL with 180 nm technology.

#### **Description:**

The explosive growth in present day technology scenario, now-a-days demand low power VLSI systems with improved performance. One of the most widely used logics in VLSI design is Domino logic. On comparing the output noise voltage with the existing logic circuits and conventional circuits the proposed domino logic has very low noise of about 1.8v.

## **Software & Hardware Skills:**

<b>Operating system Known</b>	Windows, Linux
<b>Programming Languages</b>	C-Basic, Verilog, VHDL, MASAM
<b>Tools &amp; Kits Used</b>	Altera, Spartan 3E and Vertex-5 FPGA kit
<b>EDA tools learned</b>	Cadence Virtuoso 6.1.1 V, Xilinx 14.2, Vivado for VLSI Design, MENTOR GRAPHICS, Modelsim, PSPICE

## **ACHEIVEMENTS AND APPRECIATIONS**

- [1] Received 1<sup>st</sup> prize for Full Attendance.
- [2] Received 2 times Second prize on Teacher day for Nominal Teacher.
- [3] Received best student award in schooling.
- [4] Advance Digital Design Course (ADD) and also PG Diploma in VLSI and EMBEDDED at Central Institute of Tool Design, Balanagar.
- [5] Attended workshop on FPGA in Barat Institute of Engineering and Technology.

## **Personal details:**

Date of birth : 03-03-1993

Gender : Male

Marital status : Married

Languages known : Telugu, English, Hindi and Marathi

Home Town : Nizamabad

Traits : Patience, Coordinating Skills, enthusiastic to learn new things

Date:

Place:

BANAVATH RAJENDHAR